

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,904,400 B1  
DATED : June 7, 2005  
INVENTOR(S) : Maurizio Peri et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 20,

Line 62, "memory de vice" should read as -- memory device --.

Line 66, "sense amplified" should read as -- sense amplifier --.

Column 21,

Line 10, "memory sector s" should read as -- memory sectors --.

Column 22,

Lines 4 and 5, "byte alterability" should read as -- byte alterability; --.

Line 9, "address bits" should read as -- address bus --.

Signed and Sealed this

Eleventh Day of October, 2005



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JON W. DUDAS  
*Director of the United States Patent and Trademark Office*